

REMARKS

The indicated allowance of claims 2, 4, 6 and 8 is noted, with thanks.

Turning to the art rejections, and considering first the rejection of claim 1 under 35 USC §102 (b) as being anticipated by JP 10-293559 to Hirai, *et al* (Hirai) is in error, the Examiner suggests, "Hirai teaches the charge collection and resupply circuit including: a first switch (sw111) connected between the common electrode (17) and the common voltage supply circuit [presumably the other side of sw111]; a charge collection capacitor (c11); a second switch (sw122) connected between a connection point (point at 29) of the common electrode and the first switch and the charge collection capacitor." However, the second switch (sw122) of Hirai is not between connection point 29 and charge collection capacitor (c11).

The Examiner further states that Hirai contains, "a switch control unit (FIG. 4) provided to control turning on and off of the first and second switches, the switch control unit being configured to operate such that immediately before a polarity of common voltage Vcom is inverted, the first switch (sw111) is turned off and then the second switch (sw122) is turned on (at time period t11)." However first switch (sw111) is not turned off before inversion, it is turned on at inversion. Therefore, Hirai cannot be said to teach every element of the claim 1, as amended.

Claim 1 was written to indicate that the second switch was switched on after the first switch was switched off and the second switch was switched off after the first switch was switched on. This limitation has been clarified by amendment. Switches sw111 and sw122 do not operate such that sw111 is turned off immediately before inversion, then sw122 is turned on, then sw122 is turned off, then sw111 is turned on. This sequence is claimed and is not

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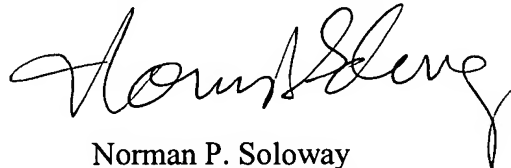
contained within Hirai. Therefore, Hirai cannot be said to teach every element of the claimed invention.

The rejection of claims 3, 5, 7 under 35 USC §103(a) as being unpatentable over Hirai in view of Harada (JP 02002041003) likewise is in error. Claims 3, 5, and 7 all depend directly or indirectly on claim 1. The deficiencies of Hirai vis-à-vis claim 1 are discussed above. It is not seen that Harada supplies the missing teachings to Hirai to achieve or render obvious claim 1 or the several claims dependent thereon. Thus, claims 3, 5 and 7 are allowable for the same reasons above addressed relative to claim 1, as well as for their own additional limitations.

Having dealt with all the objections raised by the Examiner, the Application is believed to be in order for allowance. Early and favorable action is respectfully requested.

In the event there are any fee deficiencies or additional fees are payable, please charge them (or credit any overpayment) to our Deposit Account Number 08-1391.

Respectfully submitted,



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I certify that this correspondence is being deposited with the United States Postal Service as First Class mail in an envelope addressed to MAIL STOP RCE, Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450 on July 5, 2006 at Tucson, Arizona.

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